

### **AMENDMENTS TO THE SPECIFICATION**

Please amend the specification as shown below.

Please amend the paragraph beginning on page 14, line 27, with the following amended paragraph:

It is assumed that  $m$  denotes the frame rate of an input video signal  $S1$  inputted to the frame memory 23. It is also assumed that frames sequentially inputted to the frame memory 23 are an  $\alpha$  frame, an  $\alpha+1$  frame, an  $\alpha+2$  frame, .... When the  $\alpha$  frame and the  $\alpha+1$  frame are sequentially inputted to the frame memory 23, the controller 24 controls the frame memory 23 so that the  $\alpha$  frame is outputted as an output video signal  $S2$  to the D/A converter section [25] 25-1 at a frame rate equal to  $1/2$  of the frame rate of the input video signal  $S1$  and so that the  $\alpha+1$  frame is outputted as an output video signal  $S3$  to the D/A conversion section 25-2 at a supply start time  $b$  which is delayed by  $1/m$  from a supply start time  $a$  of the  $\alpha$  frame.

Please amend the paragraph beginning on page 16, line 6, with the following amended paragraph:

Accordingly, the controller 24 controls the frame memory 23 so that the output video signal  $S2$  and the output video signal  $S3$  are respectively supplied to the D/A converter section [25] 25-1 and the D/A conversion section 25-2 alternately on a frame by frame basis at a frame rate  $m/2$  equal to  $1/2$  of the frame rate  $m$  of the input video signal  $S1$  in such a manner that the supply start time of each frame of one of the output video signals  $S2$  and  $S3$  is shifted from the

supply start time of each frame of the other by half ( $1/m$ ) of a one-frame supply time ( $2/m$ ) which is outputted.

Please amend the paragraph beginning on page 19, line 19, with the following amended paragraph:

Each of the projector 51-1 and the projector 51-2 displays a frame image corresponding to the supplied video signal by scanning the screen 52 in the horizontal direction from a pixel  $(X, Y) = (0, 0)$  to a pixel  $(X, Y) = (p, q)$  which forms an image to be displayed, at the timing based on the control of the display control section 27. The frame rate of the frame image displayed by each of the projector 51-1 and the projector 51-2 is  $m/2$ . The scan start timing of each frame displayed by one of the projectors 51-1 and 51-2 is shifted by  $1/2$  from one frame of display provided by the other, as in the case of the output video signal [SS] S2 and the output video signal S3 mentioned above with reference to Fig. 2, and the phase difference between their scans is  $1/m$ .

Please amend the paragraph beginning on page 53, line 5, with the following amended paragraph:

For example, while a projector [91-2] 51-2 is scanning a line corresponding to the  $\alpha+1$  frame on a line denoted by SCAN B on a screen [92] 52, a projector 91-3 is scanning a line corresponding to the  $\alpha+2$  frame on a line denoted by SCAN A on the screen 92. The line denoted by SCAN B is a line shifted from the line denoted by SCAN A by  $1/n$  of the number of lines of one frame. More specifically, a moving image displayed on the screen 92 is alternately rewritten by a plurality of scans including the scan A and the scan B at a time interval of  $1/m$ .